cont. B1

voltage or speed rating. The internal package node is coupled to either a package pin, an input terminal of the integrated circuit or both.

Please replace the paragraph beginning on page 11, line 17, with the following paragraph:

B2

In one embodiment, an antifuse such as the one illustrated in Fig. 12 may be utilized. The antifuse 120 includes a generally circular conductive area 121 which is in electrical contact with via 122. Via 122 may couple, e.g., to a power supply node. Via 123 is not in electrical contact with via 122. If it is desired to short via 122 and 123 together, then conductive paste or solder may be deposited so as to form an electrical contact between via 123 and via 122 and circular conductive area 121. The conductive paste may then be subject to ultraviolet curing.

In the Claims

Please cancel claims 1, 14, 26, and 28-38 and amend the following claims.

1. Canceled.

B3

2. (Amended) A package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element having a first and a second end separated by a programmable link, wherein the first end of the one-time programmable element is coupled to a power supply voltage node in the package and wherein the programmable element is one of a fuse and an antifuse; and wherein

the package includes at least one pair of programmable elements, the one pair including the one one-time programmable element and a second one-time programmable element, the second one-time programmable element having a first and second end, the first end of the second one-time programmable element coupled to a second power supply voltage node and the second end of the second one-time programmable element being coupled through an internal package node to the second end of the first -one-time programmable element.

B4 sub C1

12. (Amended) A package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element having a first and a second end separated by a programmable link, wherein the first end of the one-time programmable element

is coupled to a power supply voltage node in the package and wherein the package further comprises another programmable element coupled between the second end of the programmable element and an external package connection.

13. (Amended) A package for mounting at least one integrated circuit die, the package comprising at least one one-time programmable element having a first and a second end separated by a programmable link, wherein the first end of the one-time programmable element is coupled to a power supply voltage node in the package and wherein a second one-time programmable element is coupled in parallel with the one time programmable element and wherein the one one-time programmable element is a fuse and the second one-time programmable element is an antifuse.

14. Canceled.

15. (Amended) The package as recited in claim 2 wherein the internal package node is coupled to at least one of an external package connection and an input terminal of the integrated circuit die, after mounting of the integrated circuit die.

16. (Amended) The package as recited in claim 2 further comprising a first resistive element coupled between the internal package node and the power supply node and a second resistive element coupled between the internal package node and the second power supply node.

17. (Amended) An electronic device comprising:

a package including one or more one-time programmable elements having a first and a second end separated by a programmable link, wherein the first end of the one one-time programmable element is coupled to a power supply voltage node in the package and a second end of the programmable link is coupled to an internal package node;

at least one integrated circuit die mounted in the package; and

wherein the one one-time programmable element is part of a one-time programmable element pair, the programmable element pair including a second one-time programmable element in addition to the one one-time programmable element,

cont. B5

the second programmable element having a first end coupled to the internal package node and a second end coupled to a second power supply voltage.

21. (Amended) An electronic device comprising:

a package including one or more one-time programmable elements having a first and a second end separated by a programmable link, wherein the first end of the one one-time programmable element is coupled to a power supply voltage node in the package and a second end of the programmable link is coupled to an internal package node;

B6

at least one integrated circuit die mounted in the package; and

wherein the internal package node couples to an external package connection through another one-time programmable element.

25. (Amended) An electronic device comprising:

a package including one or more one-time programmable elements having a first and a second end separated by a programmable link, wherein the first end of the one one-time programmable element is coupled to a power supply voltage node in the package and a second end of the programmable link is coupled to an internal package node; and

at least one integrated circuit die mounted in the package and wherein a state of the programmable element specifies use of error correction code (ECC) for a cache memory on the integrated circuit.

B7

26. Canceled.

27. (Amended) The electronic device as recited in claim 17 further comprising a first resistive element coupled respectively between the internal package node and the first power supply node and a second resistive element coupled between the internal package node and the second power supply node, thereby providing a voltage divider when the first power supply node is electrically coupled to the second power supply node through the programmable element pair.

28. - 38. Canceled.